

REMARKS/ARGUMENTS

The Examiner called for placing the abstract on a separate sheet of paper. While the Examiner's request has been followed (see the enclosure), in a spirit of cooperation, the Applicant respectfully notes that there is no requirement to place an abstract from a national stage PCT application on a separate sheet of paper. The PCT Treaty governs the formality requirements of national stage international applications. See Article 27 of the PCT treaty (in appendix T of the MPEP). That article trumps 37 CFR 1.52(b)(4) cited by the Examiner.

Claim 1 has been amended to include the limitation heretofore in claim 3. Since claim 3 was indicated as being patentable over the art, this amendment should render claim 1 patentable as well.

Claim 10 has been amended to include the limitation heretofore in claim 12. Since claim 12 was indicated as being patentable over the art, this amendment should likewise render claim 10 patentable.

Claim 20 have been has been amended to include a missing period.

With respect to the clarity issues raised by the Examiner, claims 7, 8 and 17 have been amended with an eye o addressing the issue raised by the Examiner. These amendments should be self-explanatory.

Withdrawal of the rejections and allowance of the claims are respectfully requested.

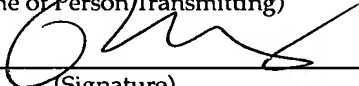
The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to deposit account no. 12-0415. In particular, if this response is not timely filed, then the Commissioner is authorized to treat this response as including a petition to extend the time period pursuant to 37 CFR 1.136 (a) requesting an extension of time of the number of months necessary to make this response timely filed and the petition fee due in connection therewith may be charged to deposit account no. 12-0415.

I hereby certify that this correspondence is being deposited with the United States Post Office with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

12-19-2006

(Date of Transmission)

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(Signature)

12-19-2006

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Abstract

A fusible resistor and method of fabricating the same is provided. The fusible resistor has a very low resistance of 20 to 470 m Ω . by depositing thin films as a fusible element made of a material with low resistivity such as copper having a temperature coefficient of over 2,000 ppm/ $^{\circ}$ C. The fusible resistor comprises a resistor body, a fusible element layer formed to surround the resistor body, caps formed to surround ends of the fusible element layer, lead wires attached to the caps, and an insulating layer for insulating the fusible element layer and the caps from outside. The thus-fabricated fusible resistor performs all functions of a use without generating excessive heat.